

FIG. 1
PRIOR ART

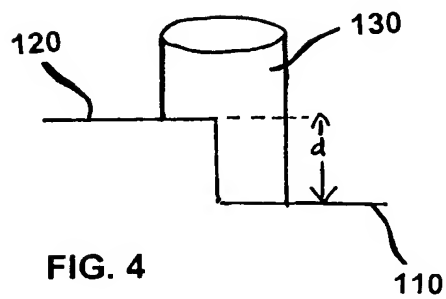
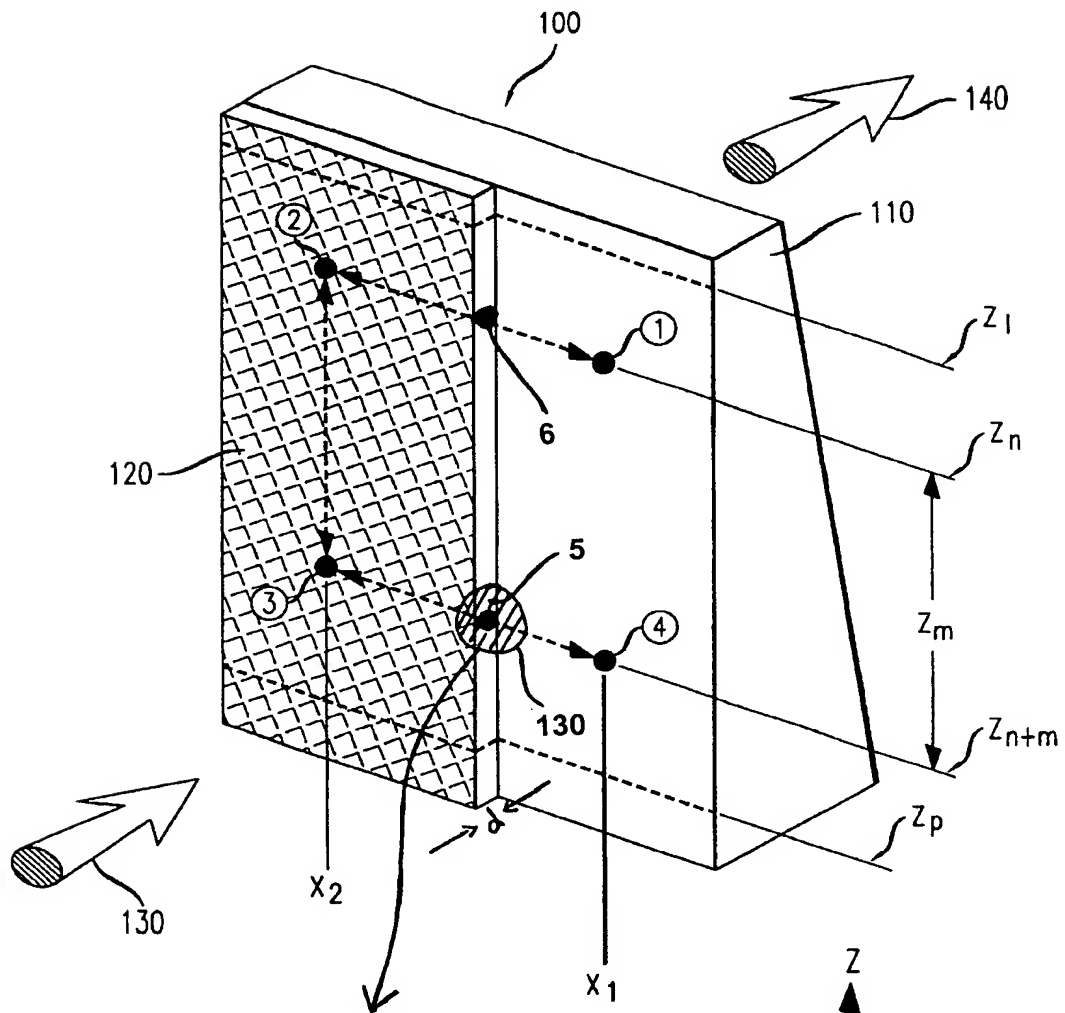


FIG. 4

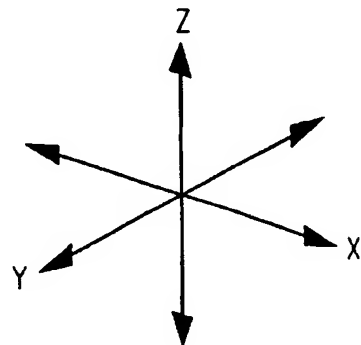
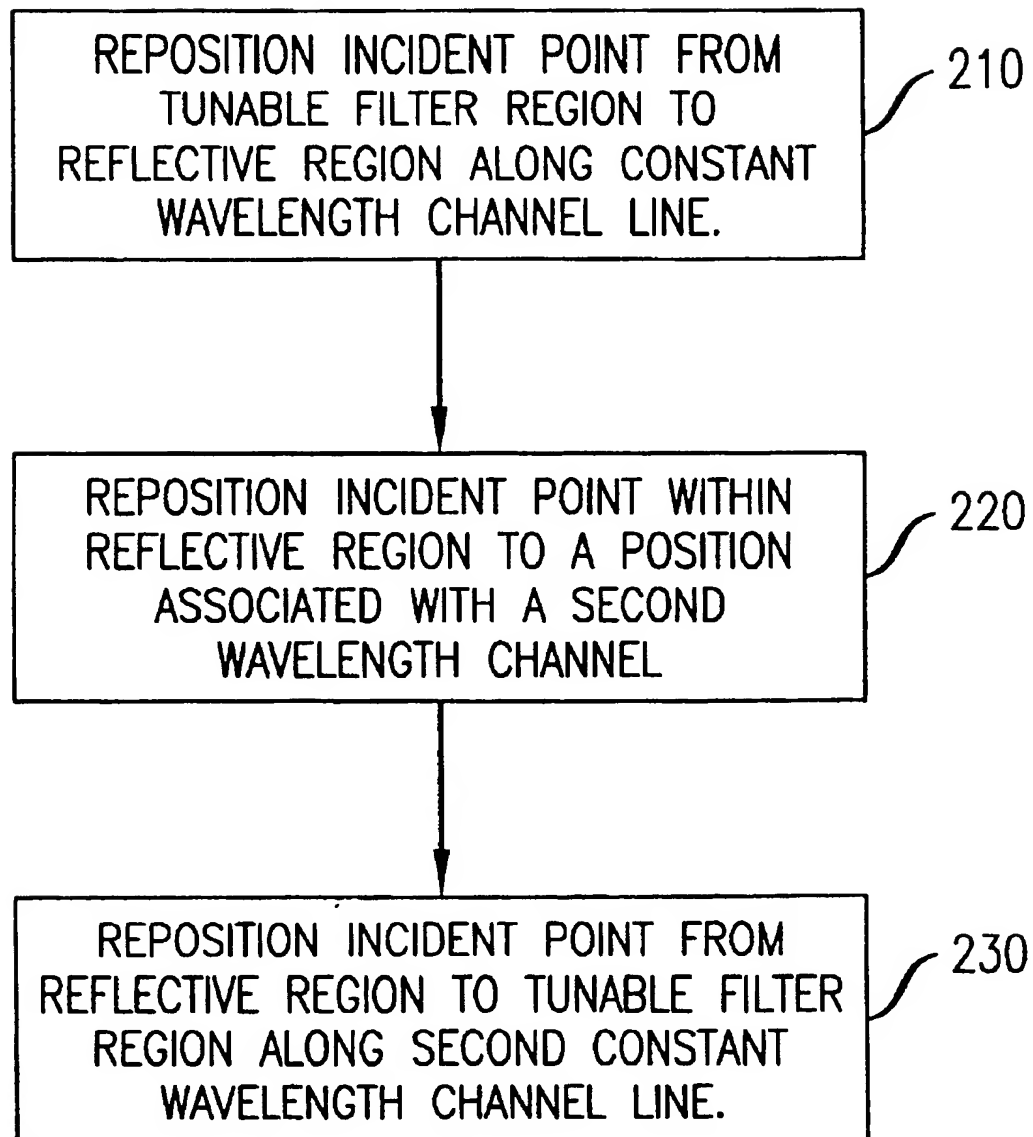
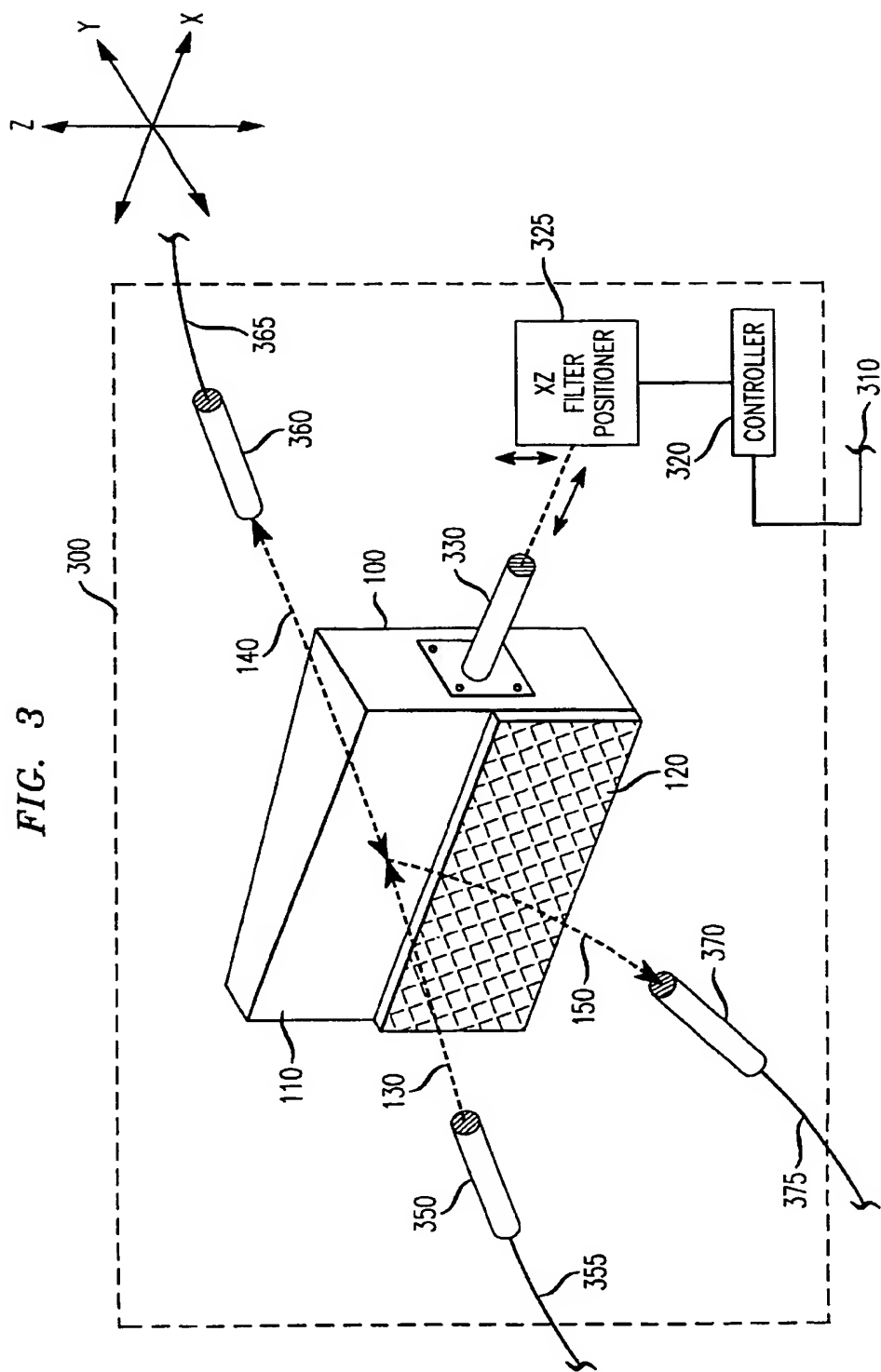


FIG. 2

PRIOR ART





PRIOR ART

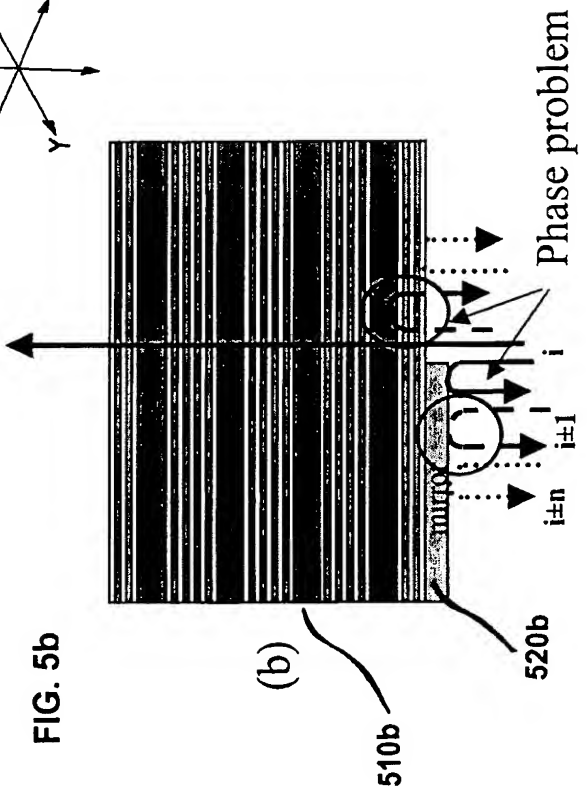
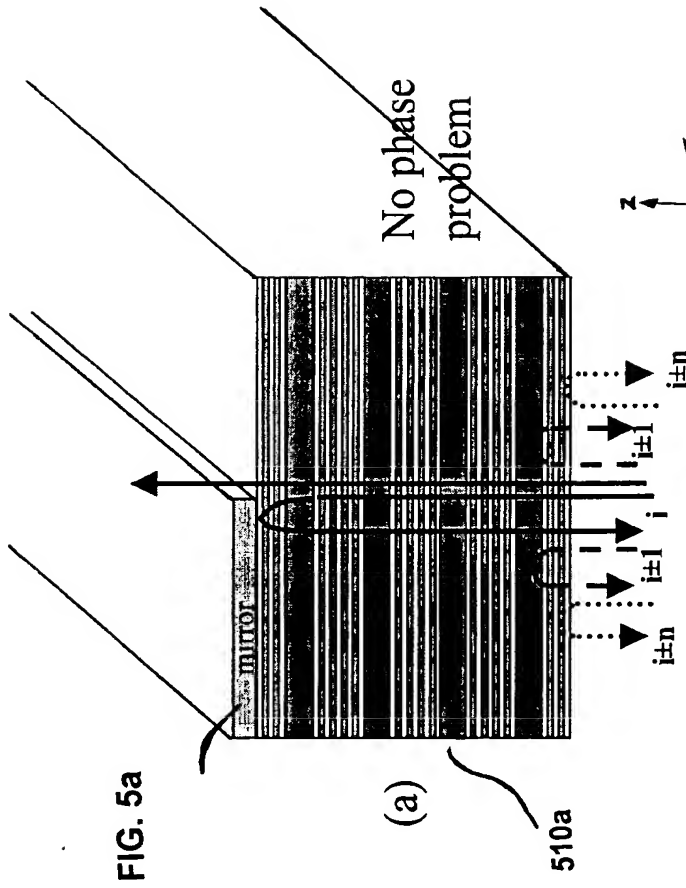


FIG. 5c

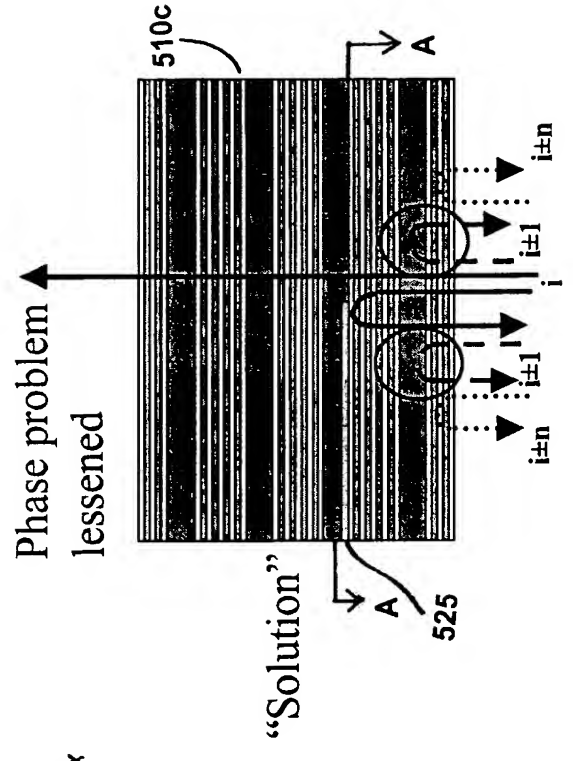


FIG. 6a

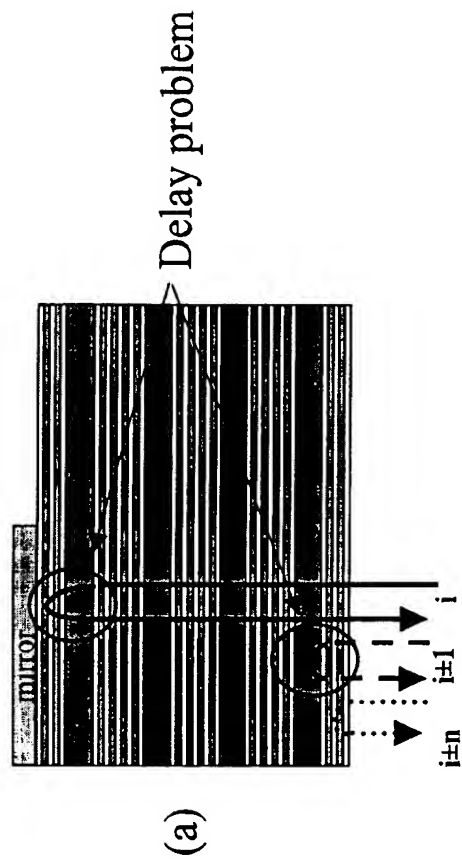


FIG. 6b

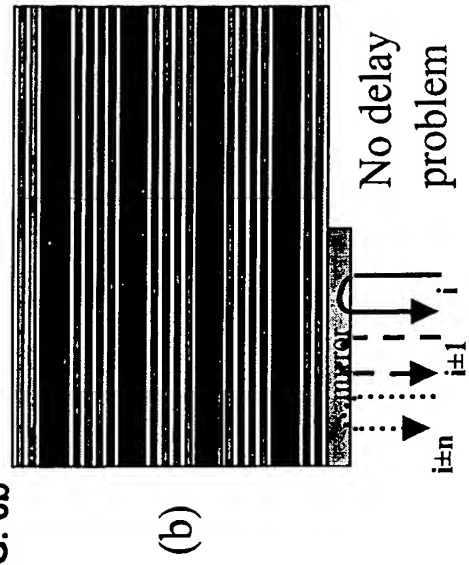
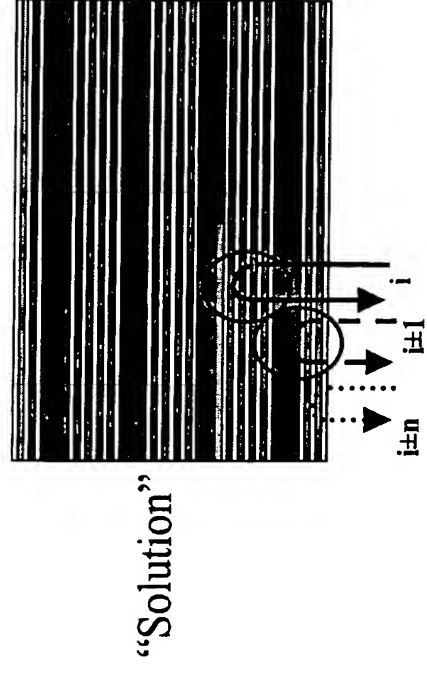


FIG. 6c

Delay problem
minimized



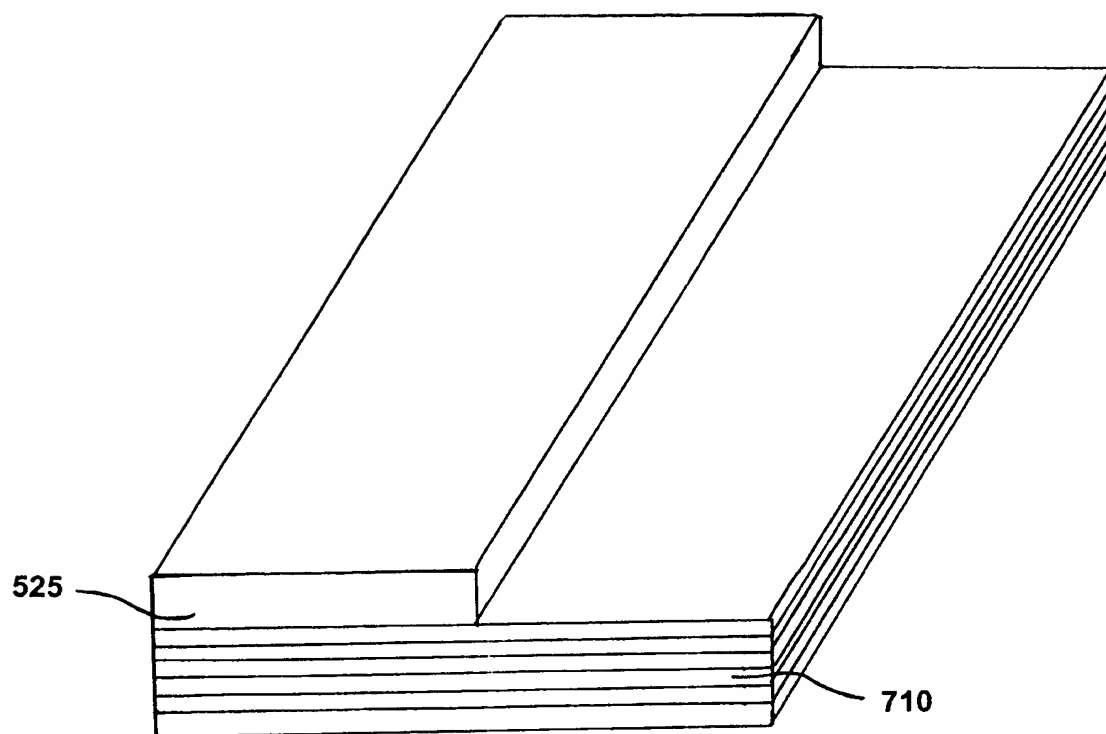


FIG. 7

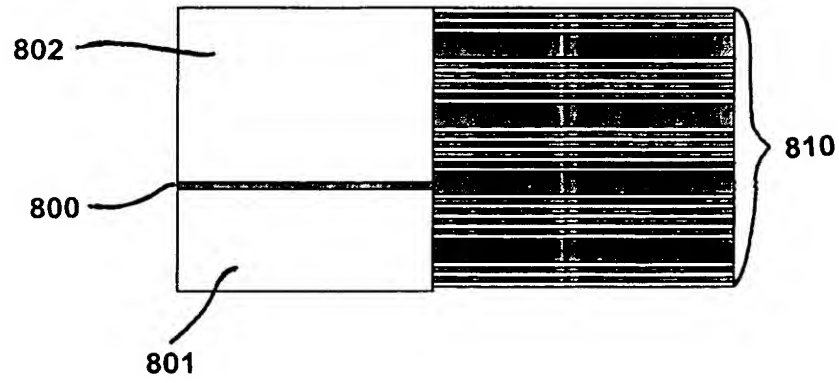


FIG. 8b

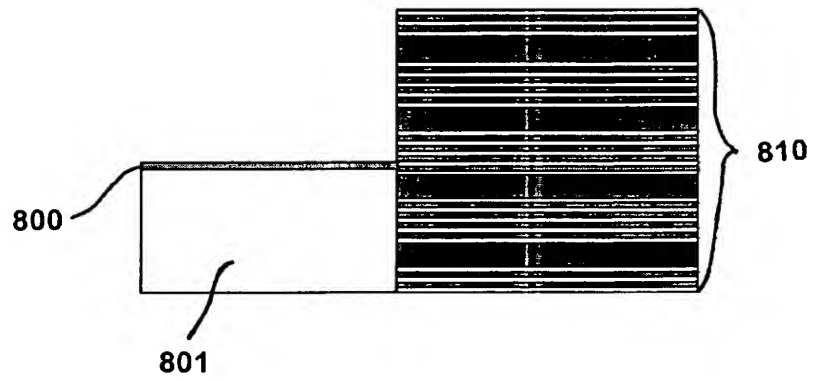


Fig. 9a

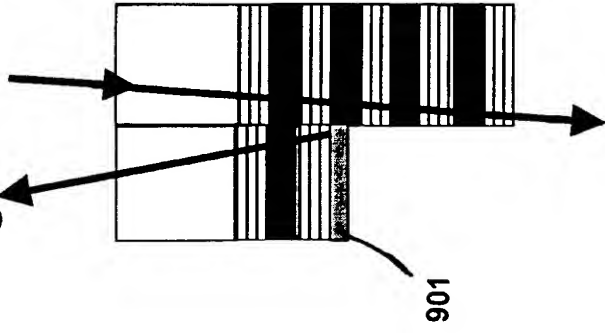


Fig. 9b

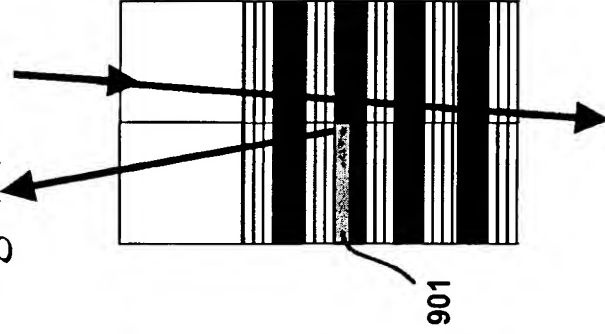


Fig. 9c

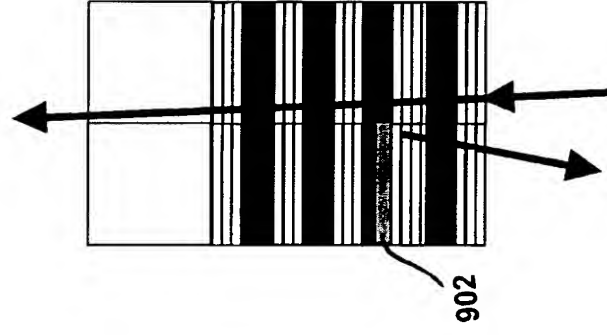


Fig. 10a

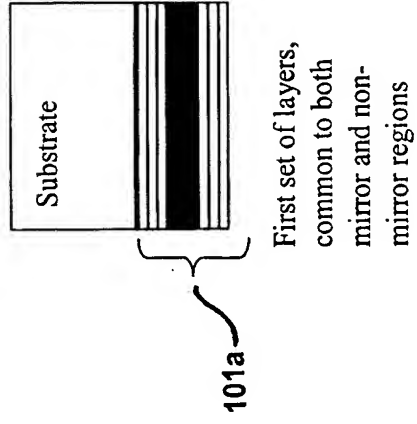


Fig. 10b

